

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Group Art Unit: 2827

Michael Farmwald et al.

Examiner: Tan Nguyen

Appln. No.: 10/716,595

Confirmation No.: 7213

Filed: November 20, 2003

For: MEMORY DEVICE AND METHOD

FOR OPERATING SAME

Customer No.: 38013

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with the duty under 37 C.F.R. § 1.56 of each individual associated with the filing and prosecution of the above-identified patent application (hereinafter, "associated individuals") to disclose all information known to that individual to be material to patentability, Applicants hereby PTO-1449 (modified) listing cited submits attached Form This submission is made in accordance with 37 references. C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609.

Per the voice communication between the Examiner and the undersigned on September 29, 2006, the filing of this supplemental information disclosure statement was necessitated by the filing of Requests for Inter Partes Reexamination for

related U.S. Patent Nos. 6,426,916 and 6,324,120. The Examiner may note that many of the listed references are dated after the effective filing date of the above-identified application. The Requests for Inter Partes Reexamination allege that such references are relevant because the claims presented in the respective patents are not entitled to their effective Many of the other references and arguments filing dates. presented in the Requests for Inter Partes Reexamination were considered in U.S. District Court and U.S. Appellate Court patent litigation proceedings involving related patents. proceedings, which included claim construction proceedings for include the related patents, Rambus Inc. v. Infineon Technologies AG et al., No. 3:00cv524 (E.D. Vir. 2001), Rambus Inc. v. Infineon Technologies AG et al., 318 F.3d 1081 (Fed. Cir. 2003), and Hynix Semiconductor, Inc. et al. v. Rambus Inc., CV 00-20905 RMW (N.D. Cal. 2005). The Hynix case specifically included the patents for which reexamination has been requested, and many of the arguments and references relied upon in the Requests for Inter Partes Reexamination were presented in those proceedings. (See e.g., reference citation nos. 17 and 31-33). If the Examiner believes that additional information relating to such proceedings would be helpful in determining patentability of the above-identified

application, Applicants would gladly provide same.

Applicants hereby expressly reserve the right to swear behind the effective dates of any of the cited references. Applicants also reserve the right to question the relevance, materiality, and/or prior art status of any of the cited references in whole, in part, or in combination, subsequent to supplemental the filing of this information disclosure statement. Further, the filing of this supplemental information disclosure statement is not to be construed as a representation that a search has, or has not, been conducted or that no better art exists.

The Examiner is respectfully requested to consider each of the cited references, to indicate such consideration by initialing in the space provided next to each cited reference on the enclosed Form PTO-1449 (modified), to sign the initialed Form PTO-1449 (modified), and to return a copy of the same with the next communication to the Applicants.

In accordance with 37 C.F.R. § 1.98(a), only copies of the cited references which are not U.S. patents or U.S. patent application publications are being submitted herewith. However, copies of the cited references which are U.S. patents or U.S. patent application publications will be submitted at the request of the Examiner. In considering these cited references, it may

be noted by the Examiner that certain of the references may contain markings, underlinings, and/or other notations. These markings, underlinings, and/or other notations are not to be construed as drawing the Examiner's attention either to selected parts or away from other parts of these cited references. Any such markings were either present on the copies of these cited references when obtained by the associated individuals, or were made thereon during the study of these cited references by the associated individuals.

In accordance with 37 C.F.R. § 1.97(b), this information disclosure statement is being filed (i) within three months of the filing date of the above-identified patent application; (ii) within three months of the date upon which the above-identified patent application entered the national stage as set forth in 37 C.F.R. § 1.491; (iii) before the mailing date of a first Office merits for the above-identified Action on the application; or (iv) before the mailing date of a first Office Action after the filing of a request for continued examination under 37 C.F.R. § 1.114. Accordingly, no statement or fee is required.

Please charge any shortage in fees due in connection with the filing of this communication to Deposit Account No. 50-0206, and please credit any excess fees to such deposit account.

Respectfully submitted,

Hunton & Williams LLP

Thomas E. Anderson

Registration No. 37,063

TEA/asc

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Date: October 2, 2006

PTO/SB/08A (08/03) (modified)
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO

1 of 5

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Application Number	10/716,595
Filing Date	November 20, 2003
First Named Inventor	Michael Farmwald
Art Unit	2827
Examiner Name	Tan Nguyen
Attorney Docket Number	57941.000062

U.S. PATENT DOCUMENTS

*Examiner Initials	Cite No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
-	1.	US- 6,426,916	07-30-2002	Farmwald, et al.	
	2.	US- 4,933,910	06-12-1990	Olson, et al.	
		US-			
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		US-			

EXAMINER SIGNATURE

DATE CONSIDERED

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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Application Number 10/716,595	Substitu	te for	form 1449/PTO	Anni	ication Number	10/716,595	_	
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STATEMENT BY APPLICANT (Use as many sheets as necessary) Art Unit Examiner Name Tan Nguyen Sheet 3 of 5 Attorney Docket Number Total (When appropriate), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue Include name of the author (in CAPITAL LETTERS), title of the hearticle (when appropriate), title of th	INF	OF	MATION DISCLOSURE					
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OTHER DOCUMENTS - NON-PATENT LITERATURE DOCUMENTS Comparison Comp				Exan	niner Name	Tan Nguyen		
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5. Hyundai, SDRAM Timing Diagram, Rev.1.2, published December 1999 ("Hynix-2")								T
6. Hyundai, DDR SDRAM Device Operation, Rev. 0.2, published December 1998 ("Hynix-3") 7. Claim Chart for Claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 40-41 based on Park, with additional references to the JEDEC Standard 8. Claim Chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard 9. Claim Chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover 10. Claim Chart for claims 1-41 of the '916 patent based on the Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover 11. Patent Family Tree Chart 12. File History of the '916 patent 13. File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history) 14. Rambus Inc. v. Samsung Electronics Co., Ltd., et al., No. C05 02298 RMW (N. D. Cal.		4.	Hyundai, HY5DV651622, Rev. 0.9, pub	olished	January 2000 ("Hynix-1"	")		
6. Hyundai, DDR SDRAM Device Operation, Rev. 0.2, published December 1998 ("Hynix-3") 7. Claim Chart for Claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 40-41 based on Park, with additional references to the JEDEC Standard 8. Claim Chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard 9. Claim Chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover 10. Claim Chart for claims 1-41 of the '916 patent based on the Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover 11. Patent Family Tree Chart 12. File History of the '916 patent 13. File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history) 14. Rambus Inc. v. Samsung Electronics Co., Ltd., et al., No. C05 02298 RMW (N. D. Cal.					-			
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		OTHER DOCUMENTS - NON-	-PATENT LI	ITERATURE	DOCUMENTS			
*Examiner	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the						LATION	
Initials	No.	item (book, magazine, journal, senal, number(s), publishe	symposium, catalo er, city and/or coun	log, etc.), date, page ntry where published	(s), volume-issue	YES	NO	
	15.	Samsung Electronics Co., Ltd., v. Memorandum Opinion filed July 18, 200	Rambus Inc., 06.	, No. 3:05cv40)6 (E. D. Va. 2006),			
	16.	Hynix Semiconductor, Inc. et al. v. Ran Joint Claim Construction and Prehearing	mbus Inc., No.	CV 00-20905 F Pursuant to Pater	RMW (N. D. Cal. 2005), nt Local Rule 4-3			
	17.	Hynix Semiconductor, Inc. et al. v. Rar Claim Construction Order filed Nov. 15,	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005), Claim Construction Order filed Nov. 15, 2004.					
	18.	Selected Excerpts from the Trial Transcript from Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N. D. Cal. 2005)						
	19.	Rambus Inc. v. Infineon Technologic Memorandum Opinion	Rambus Inc. v. Infineon Technologies AG et al., No. 3:00cv524 (E. D. Vir. 2001), Memorandum Opinion					
	20.	Rambus Inc. v. Infineon Technologies A	G et al., 318 F	F.3d 1081 (Fed.	Cir. 2003)			
	21.	Waters, "Hynix Told to Pay Damages to	Rambus", Fina	ancial Times, Ap	oril 24, 2006.			
	22.	Poletti, "Rambus wins victory from Supr Other Lawsuits seeking Royalties", San	reme Court - W Jose Mercury	Vin could Lead to News, October	o Success for Rambus' 7, 2003.			
	23.	least as far as the memory industry is	Kanellos, "Future of memory market hangs on Rambus trials - It's the trial of the century, at least as far as the memory industry is concerned, CNET News.com, February 12, 2001, http://news.com.com/Future+of+memory+market+hangs+on+Rambus+trials/2100-1001_3-252404.html					
	24.	In the Matter of Rambus, Inc., FTC Doc	In the Matter of Rambus, Inc., FTC Docket No. 9302 Opinion of the Commission					
25. In the Matter of Rambus, Inc., FTC Docket No. 9302 Concurring Opinion of Commission Jon Leibowitz								
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Initials	No.	number(s), publishe	item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published				
	26.	In the Matter of Rambus, Inc., FTC Do	ocket No. 9302 Order Revers	ing and Vacating Initial			
	27.	Rambus' Final Infringement Contentions	s in the Hynix Litigation				
	28.	Exhibit A (accused products) to Rambus	Exhibit A (accused products) to Rambus' Final Infringement Contentions				
	29.	Exhibit P ('916 infringement chart) to Re	Exhibit P ('916 infringement chart) to Rambus' Final Infringement Contentions				
	30.	Exhibit R (Exemplary data sheet) to Rar	mbus' Final Infringement Conte	entions			
	31.	Request for <i>Inter Partes</i> Reexaminatio 6,324,120.	on under 35 U.S.C. §§ 311-3	18 for U.S. Patent No.			
	32.	Request for <i>Inter Partes</i> Reexaminatio 6,426,916.	on under 35 U.S.C. §§ 311-3	18 for U.S. Patent No.			
	33.	Special Verdict Form filed April 24, 2006 No. CV 00-20905 RMW (N. D. Cal. 2005	6, in Hynix Semiconductor, Inc 5)	:. et al. v. Rambus Inc.,			
EXAMI	VER:	SIGNATURE	DATE CONSIDERED				
*EXAMI citation	NER	t: Initial if reference considered, whether or tin conformance and not considered. Include	not citation is in conformance copy of this form with next com-	with MPEP 609. Draw li munication to applicant.	ine thro	ough	

In place of PTO-1449			F COMMERCE MARK OFFICE		Complete if Known
Form				Application Number	Ex Parte Reexamination of 6,426,916
INFORM	ATION DISCLOSU APPLICA		EMENT BY	Filing Date	Herewith
(u:	se as many sheets	as neces	sary)	Applicant(s)	
				Art Unit	To be Determined
				Examiner Name	To be Determined
SHEET	1 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	38512.3

U. S. PATENT DOCUMENTS						
Examiner's Initials	Exhibit	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		
	Α	6,426,916	07-30-2002	Farmwald, et al.		
	В	5,590,086	12-31-1996	Park, et al.		
	F	4,480,307	10-30-1984	Budde, et al.		
	G	4,315,308	02-09-1982	Jackson		
	J	4,933,910	06-12-1990	Olson, et al.		
	К	5,361,277	03-30-1989	11-01-1994		

			FOREIGN PATENT D	DOCUMENTS	
Examiner's Initials	Exhibit	Foreign Patent Document	Publication Date MM-DD-YYYY	Patentee or Applicant of Cited Document	Translation Y/N
	ı	JP S56-047996	04-30-1981	Yoshida	Yes
	L	GB 2197553	05-18-1988	Lofgren, et al.	
		NON	-PATENT LITERATU	RE DOCUMENTS	
Examiner's Initials	Exhibit		volume-issue numbe	LLETTERS), title of the article, title of er(s), publisher, city/country where publications.	ished
Joint Electron Device Engineering Council (JEDEC) Standard No. 21-C, Revision 9 public ("JEDEC Standard")					on 9 published in 1999
	D, Tab 1	Hyundai, <u>HY5DV65</u>	51622, Rev. 0.9, publi	shed January 2000 ("Hynix -1")	
	D, Tab 2	Hyundai, SDRAM 1	liming <u>Diagram</u> , Rev.	1.2, published December 1999 ("Hynix	(-2")
	D, Tab 3	Hyundai, <u>DDR SDF</u>	RAM DEVICE OPERA	TION, Rev. 0.2, published December	1998 ("Hynix -3")
E, Tab 1 Intel Corporation, <u>iAPX 432 Interconnect Architecture Reference Manual</u> , published in 1982 (** Manual**)					
	E, Tab 2	Intel Corporation, Ememory control uni	lectrical Specification t (MCU), published M	s for IAPX 43204 Bus-Interface Unit (B arch 1983 ("the IAPX Specification")	IU) and iAPX 43205
	Н	Rau et al., The Cydra January 1989 ("Rau"	5 Departmental Superc	omputer Design Philosophies, Decisions, a	and Tradeoffs, published i

Examiner	Date	
Signature	Considered	

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

In place of PTO-1449			F COMMERCE MARK OFFICE		Complete if Known
Form					
111500114	TON BIOGI COL			Application Number	Ex Parte Reexamination of 5,794,060
INFORMA	TION DISCLOSU APPLICA		EMENI BY	Filing Date	Herewith
(use	as many sheets	as neces	sary)	Applicant(s)	
				Art Unit	To be Determined
				Examiner Name	To be Determined
SHEET	2 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	33142.9

1		NON-PATENT LITERATURE DOCUMENTS
Examiner's Initials	Exhibit	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	М	Johnson et al., A Variable Delay Line PLL for CPU - Coprocessor Synchronization, IEEE Journal of Solid-State Circuits, vol. 23, no. 5, published October 1988 ("Johnson")
	N	Intel Corporation, Memory Components Handbook, Chapter 1 and Chapter 3, Application Note AP-132, published in 1982 and 1985 ("the iRAM reference")
	0	Claim chart for claims 1-5, 10, 12-16, 18-20, 23, 25-32, 35, 37, and 39-41 based on Park, with additional reference to the JEDEC Standard
	Р	Claim chart for claims 1-5, 9-10, 12-16, 18-20, 23, 25-27, 30-32, 35, 37, and 40-41 based on the JEDEC Standard
	Q	Claim chart for claims 1-41 of the '916 patent based on the iAPX Manual, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover
	R	Claim chart for claims 1-41 of the '916 patent based on Budde, with additional reference to Rau, Yoshida, Olson, Johnson, Lofgren, and Grover
	s	Patent Family Tree Chart
	т	File History of the '916 patent
	U	File History of U.S. Patent No. 6,452,863 ("the '863 parent patent") (Tab 1 identifies the original disclosure of the '863 parent patent, and Tab 2 identifies the remaining portions of the file history)
	٧	Rambus Inc. v. Samsung Electronics Ltd., et al. No. C 05 02298 RMW (N.D. Cal. 2006), Preliminary Infringement Contentions
	w	Samsung Electronics Co., Ltd., v. Rambus Inc. No. 3:05cv406 (E.D. Va. 2006), Memorandum Opinion filed July 18, 2006
	×	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N.D. Cal. 2005), Joint Claim Construction and Prehearing Statement Pursuant to Patent Local Rule 4-3
	Y	Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N.D. Cal. 2005), Claim Construction Order filed Nov. 15, 2004
	Z	Selected Excerpts from the Trial Transcript from Hynix Semiconductor, Inc. et al. v. Rambus Inc., No. CV 00-20905 RMW (N.D. Cal. 2005).
	AA	Rambus Inc. v. Infineon Technologies et al., No. 3:00cv524 (E.D. Vir. 2001), Memorandum Opinion
	AB	Rambus Inc. v. Infineon Technologies AG, 318 F.3d 1081 (Fed. Cir. 2003)

Examiner	Date
Signature	Considered

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.

				Complete if Known		
				Application Number	Ex Parte Reexamination of 5,794,060	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			EMENT BY	Filing Date	Herewith	
			ary)	Applicant(s)		
				Art Unit	To be Determined	
				Examiner Name	To be Determined	
SHEET	3 (Exhibits)	OF	3 (Exhibits)	Attorney Docket Number	33142.9	

	NON-PATENT LITERATURE DOCUMENTS				
Examiner's Initials	Exhibit	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published			
	AC	Waters, Hynix Told To Pay Damages To Rambus, Financial Times, April 24 2006			
	AD	Poletti, Rambus wins victory from Supreme Court - Win Could Lead To Success For Rambus' Other Lawsuits Seeking Royalties, San Jose Mercury News, Oct. 07, 2003			
	AE	Kanellos, Future of memory market hangs on Rambus trials - It's the trial of the century, at least as far as the memory industry is concerned, CNET News.com, Feb. 12, 2001, http://news.com.com/Future+of+memory+market+hangs+on +Rambus+trials/2100-1001_3-252404.html			
	AF	Appeal Board Decision dated Feb. 12, 2004, Summary of Facts and Submissions for the oppositions filed against European Patent No. 0 525 068			
	AG	Summary of Facts and Submissions for the oppositions filed against European Patent No. 1 004 956			
	AH, Tab 1	In the Matter of Rambus, Inc., FTC Docket No. 9302, Opinion of the Commission			
	AH, Tab 2	In the Matter of Rambus, Inc., FTC Docket No. 9302, Concurring Opinion of Commissioner Jon Leibowitz			
	AH, Tab 3	In the Matter of Rambus, Inc., FTC Opinion Docket No. 9302, Order Reversing and Vacating Initial Decision			
	Al, Tab 1	Rambus' Final infringement Contentions in the Hynix Litigation			
	Al, Tab 2	Exhibit A (accused products) to Rambus' Final infringement Contentions			
	Al, Tab 3	Exhibit P ('916 infringement chart) to Rambus' Final infringement Contentions			
	Al, Tab 4	Exhibit R (Exemplary data sheet) to Rambus' Final infringement Contentions			
	LA.	CD-ROM including present Request for Inter Partes Reexamination and Exhibits			

Examiner	Da	ate	
Signature	Co	onsidered	

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